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FIG. 1A

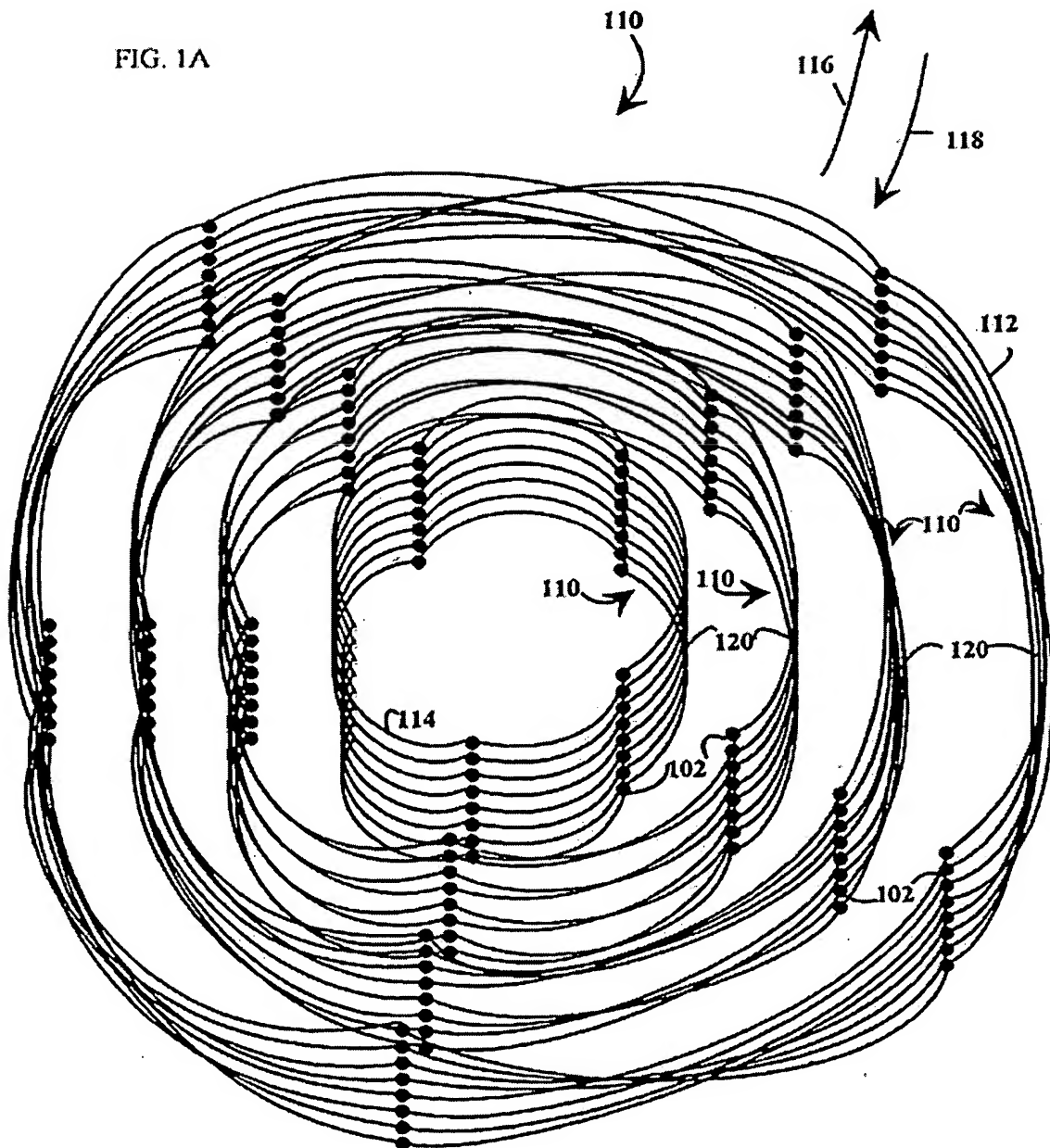
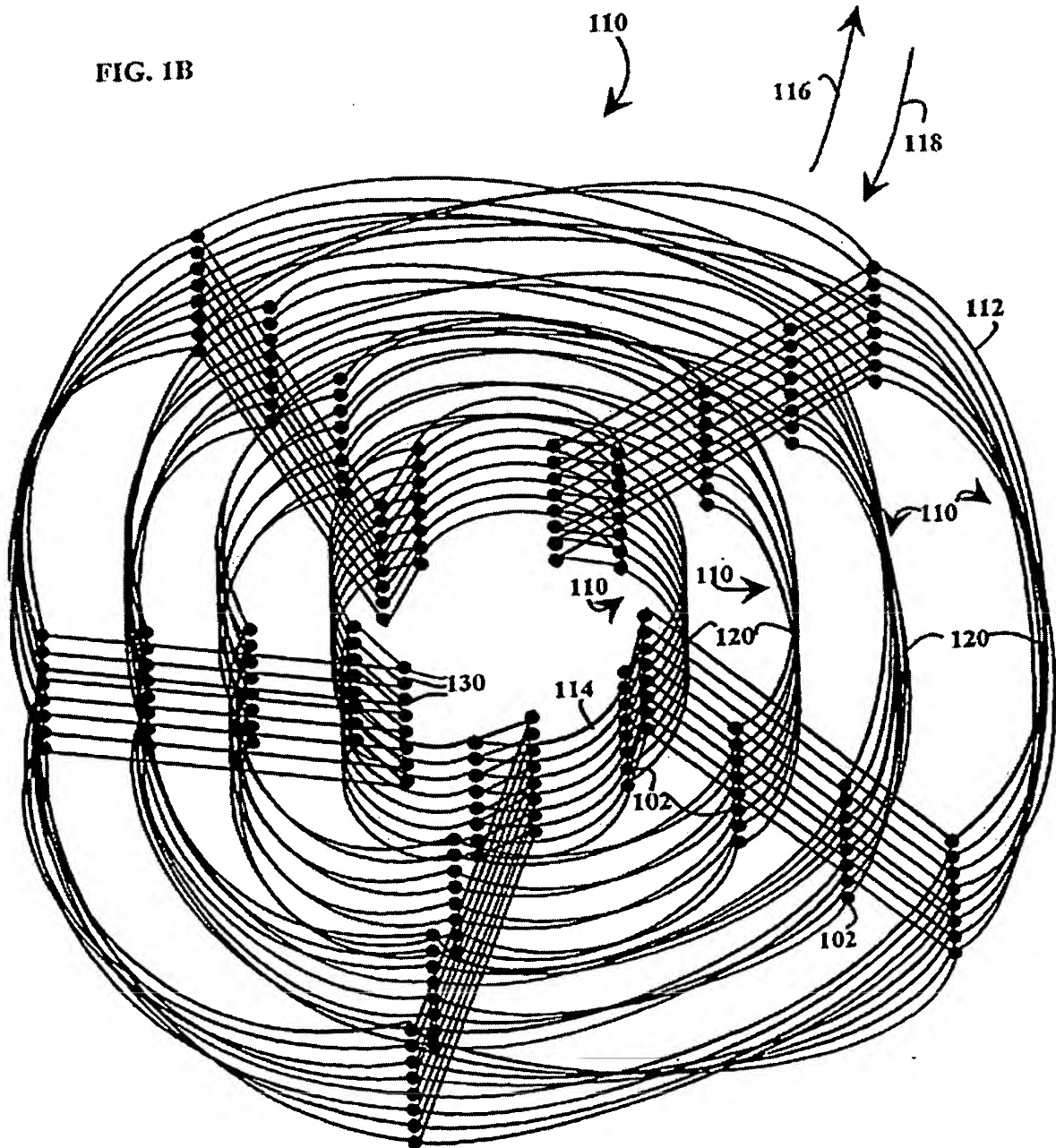


FIG. 1B

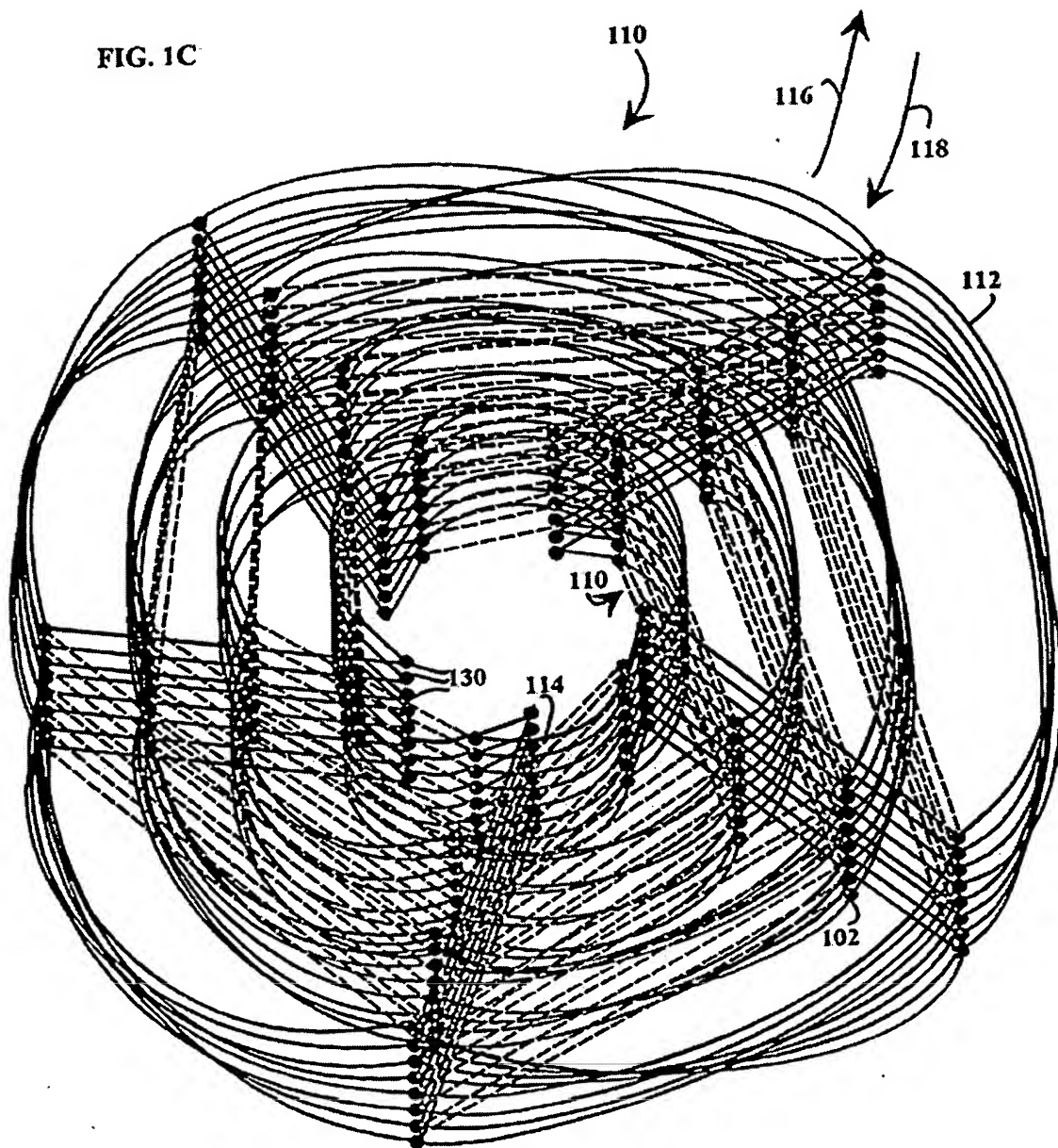


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FIG. 1C

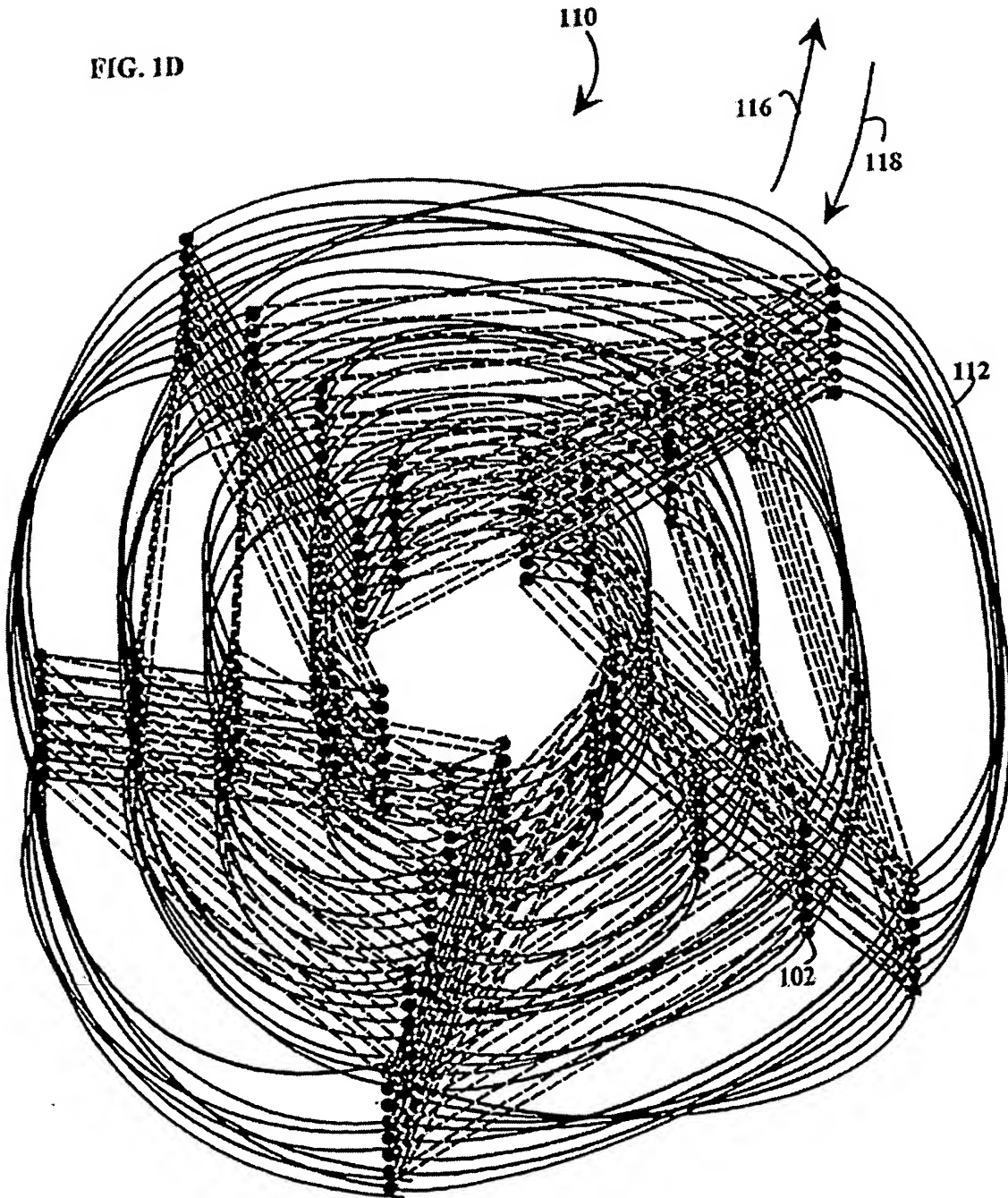


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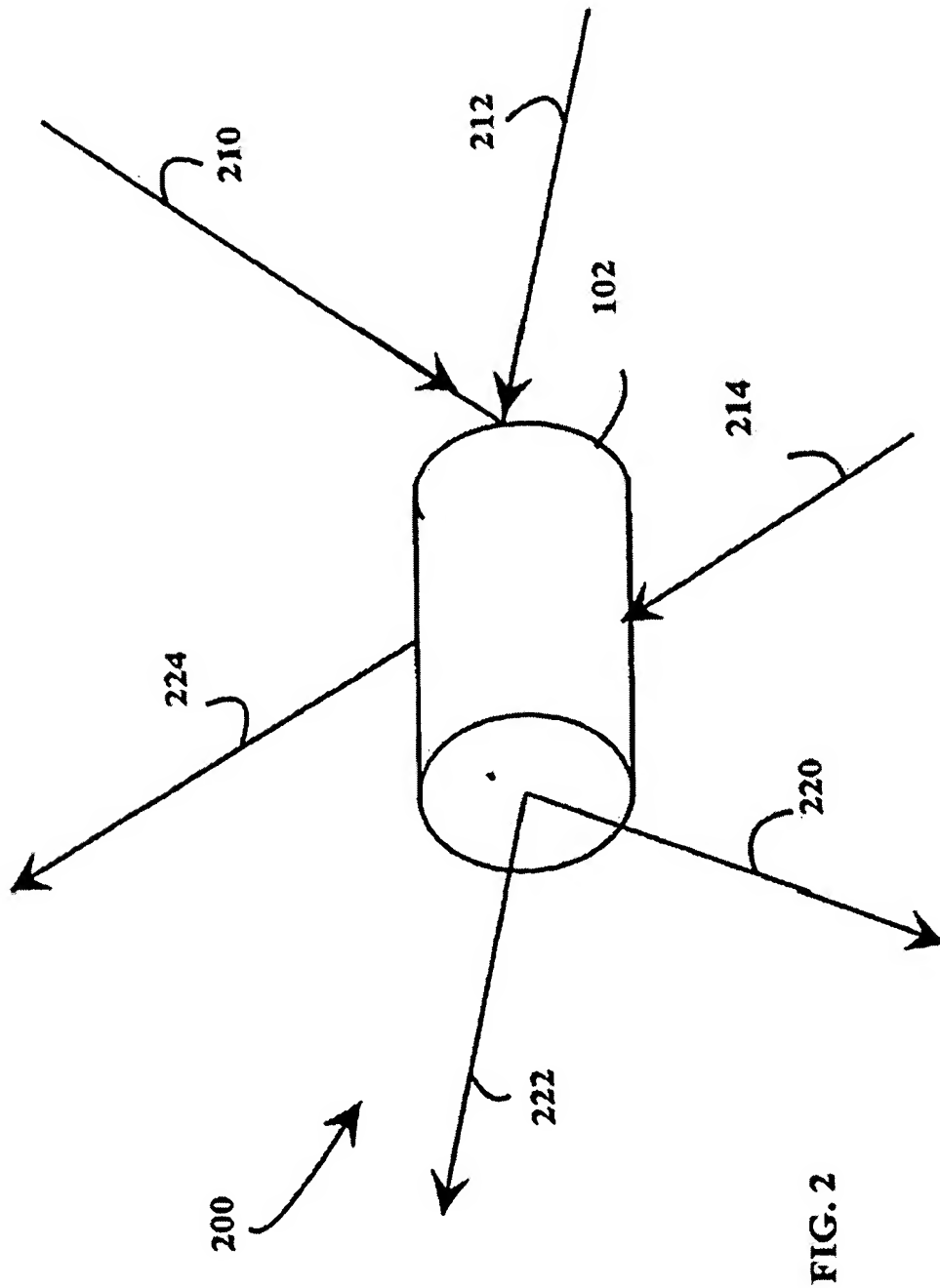
FIG. 1D



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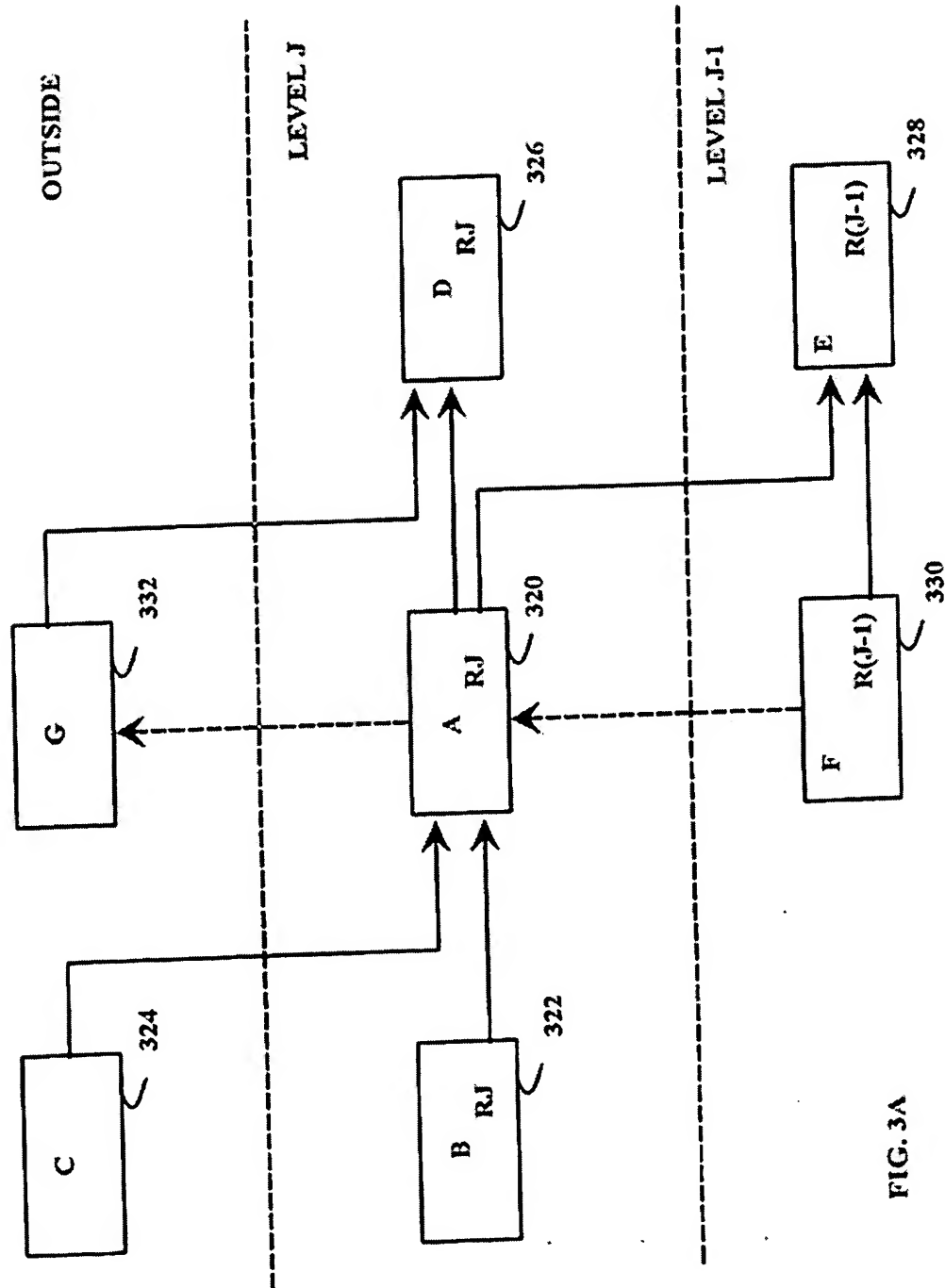
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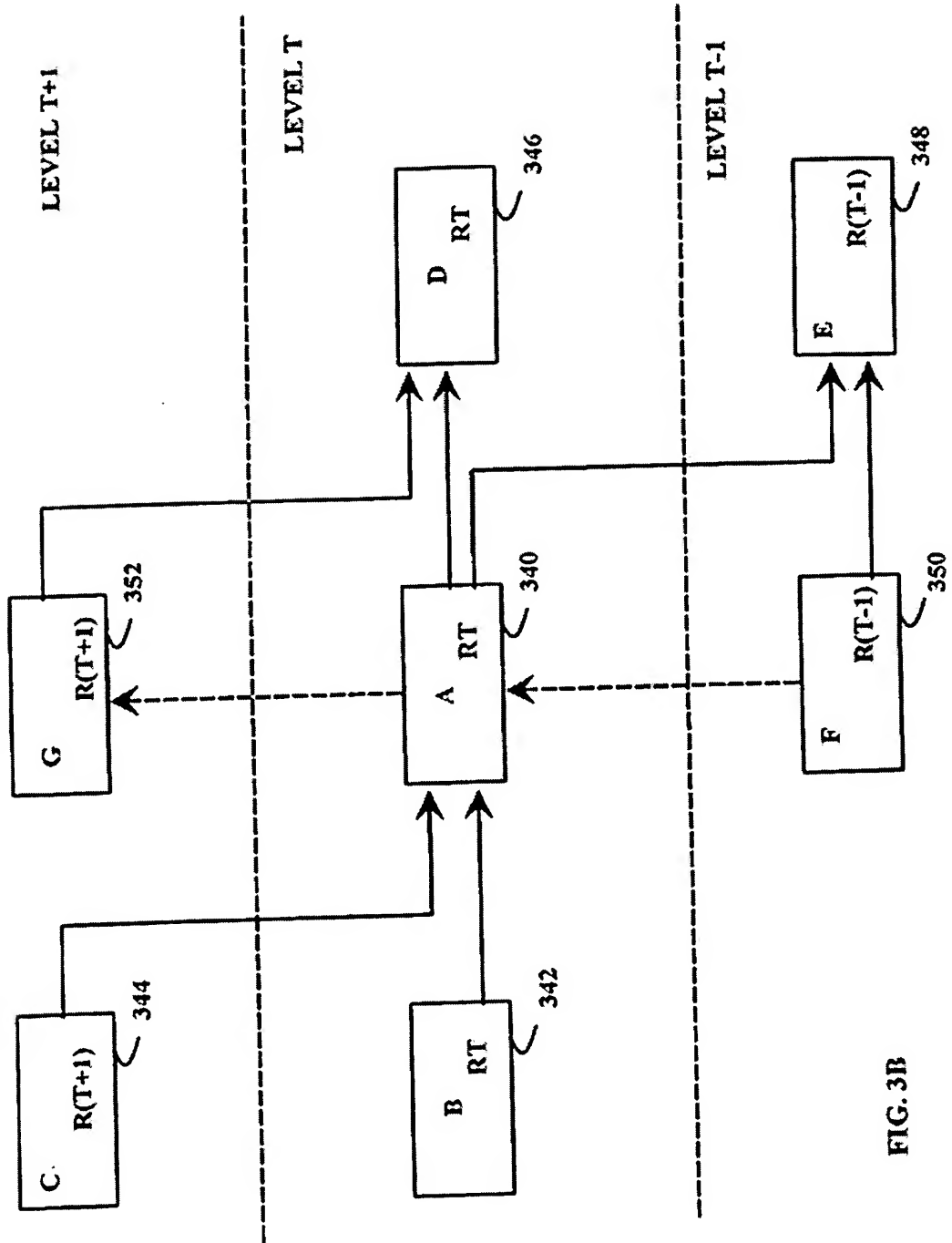
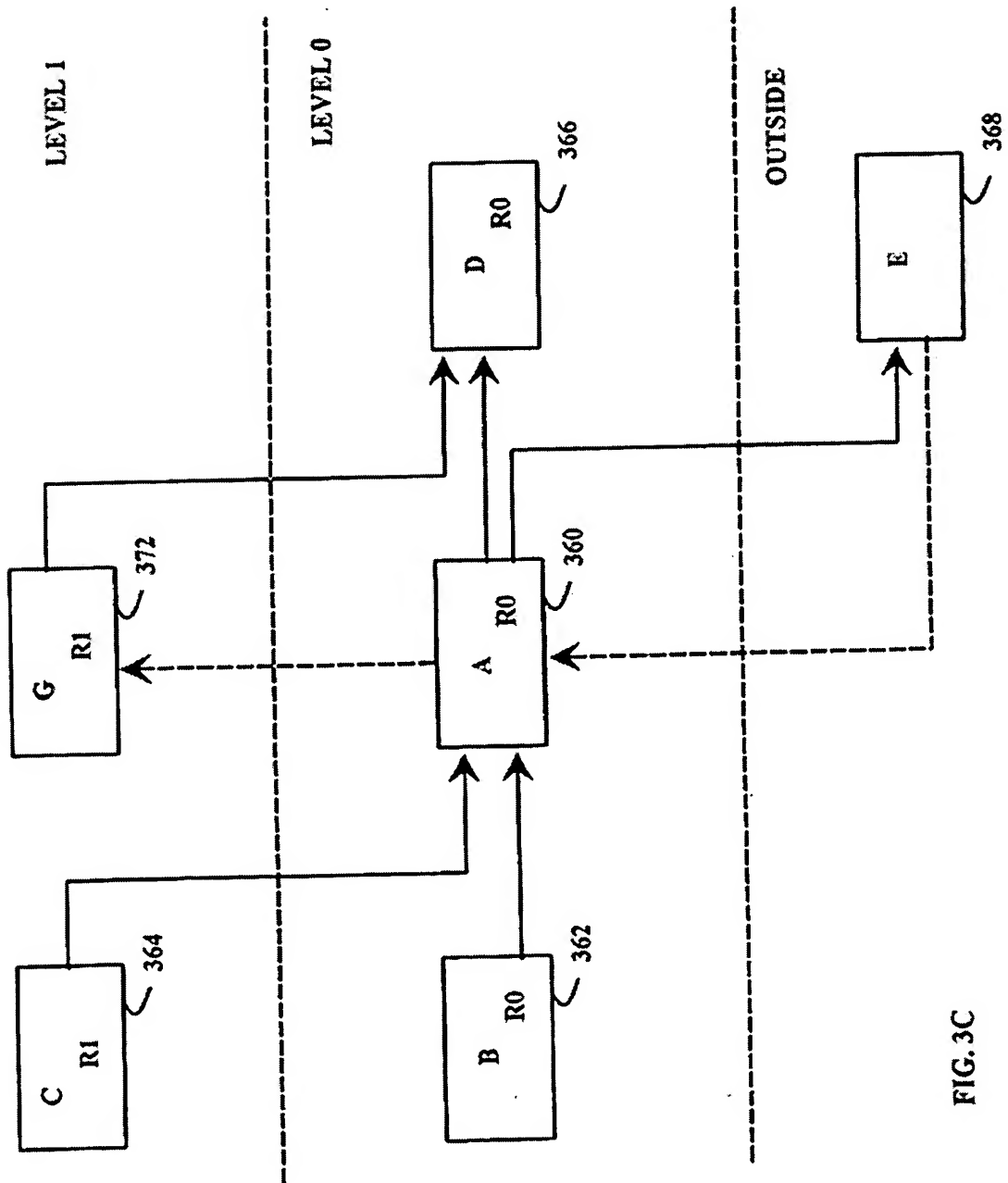


FIG. 3B

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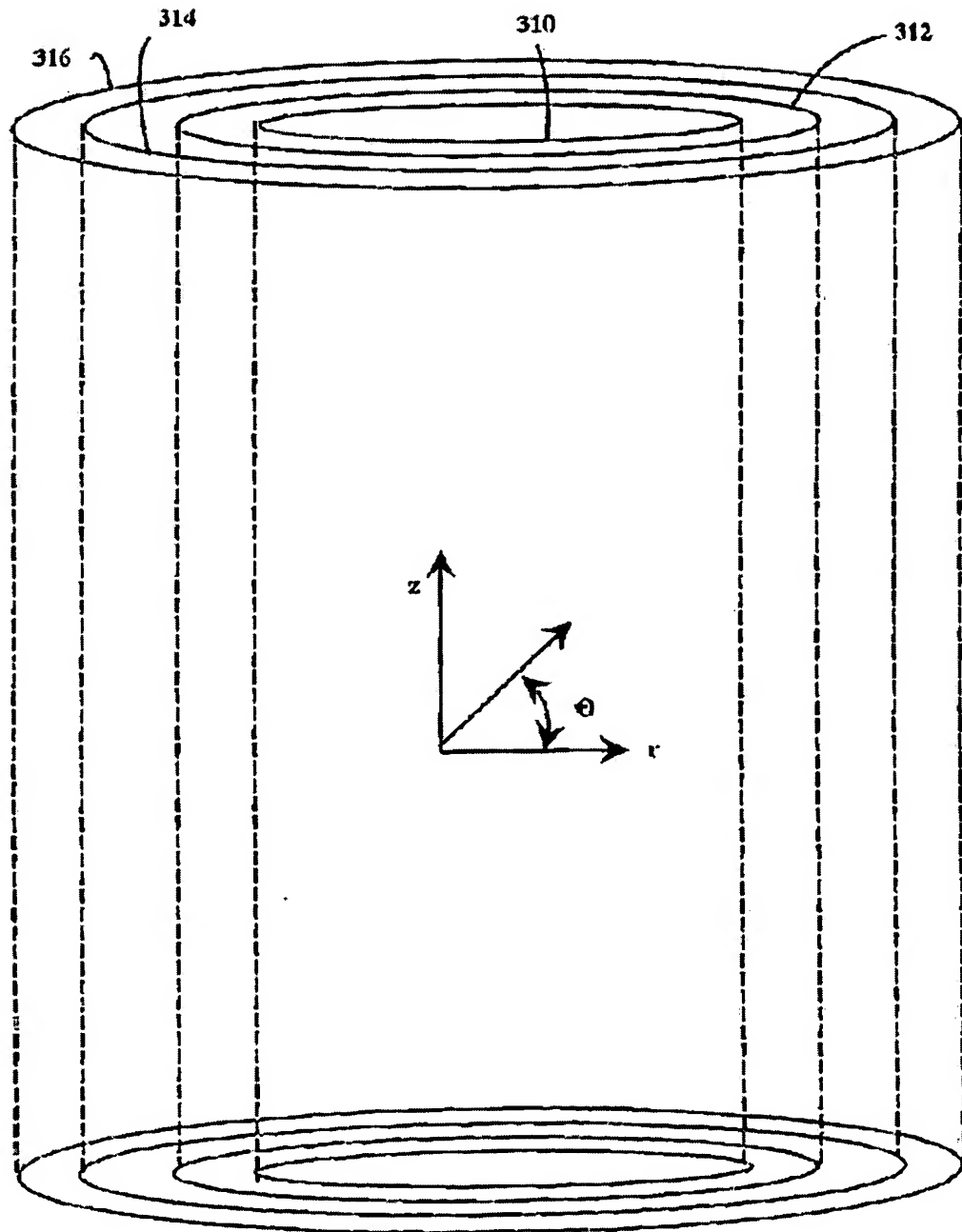
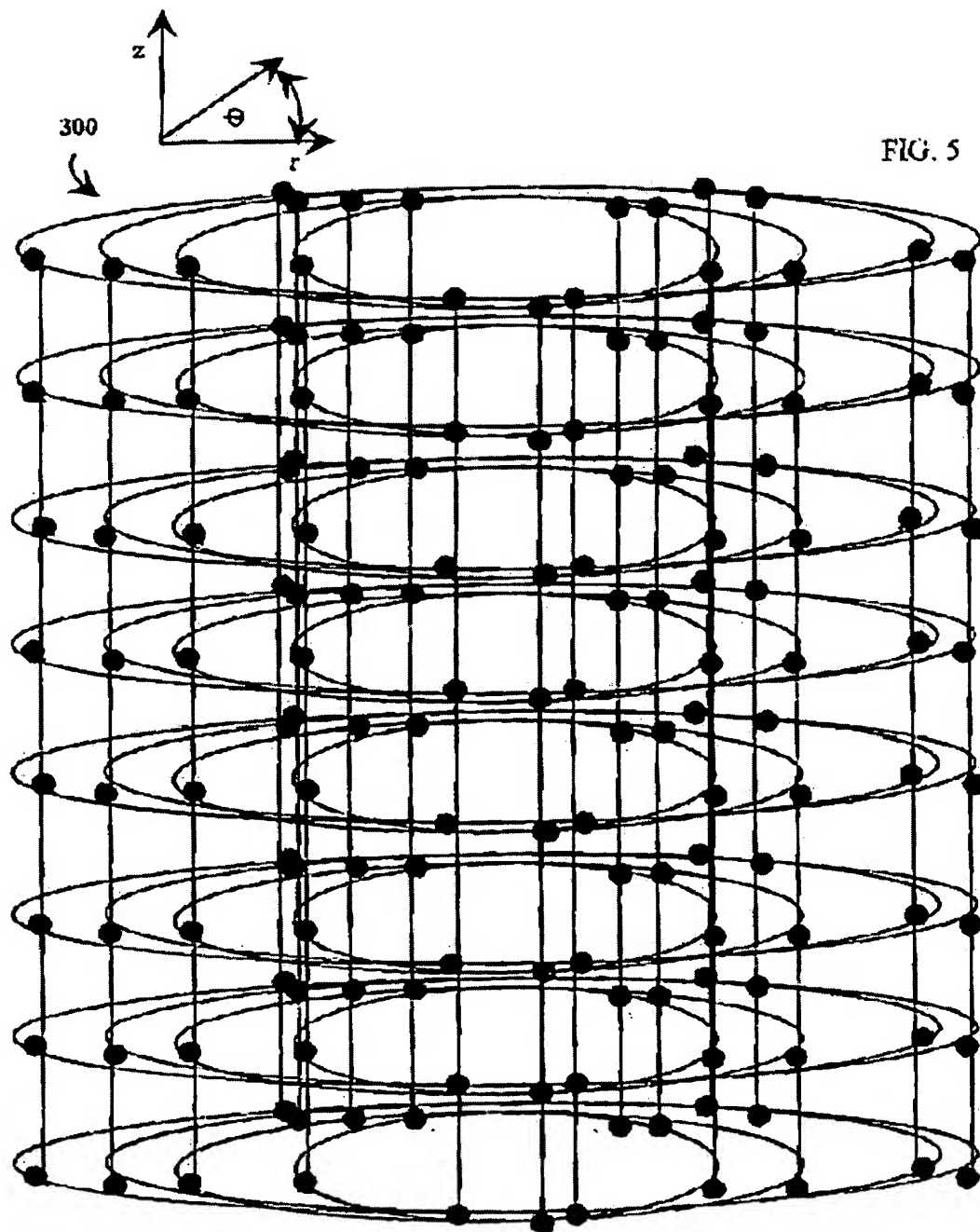
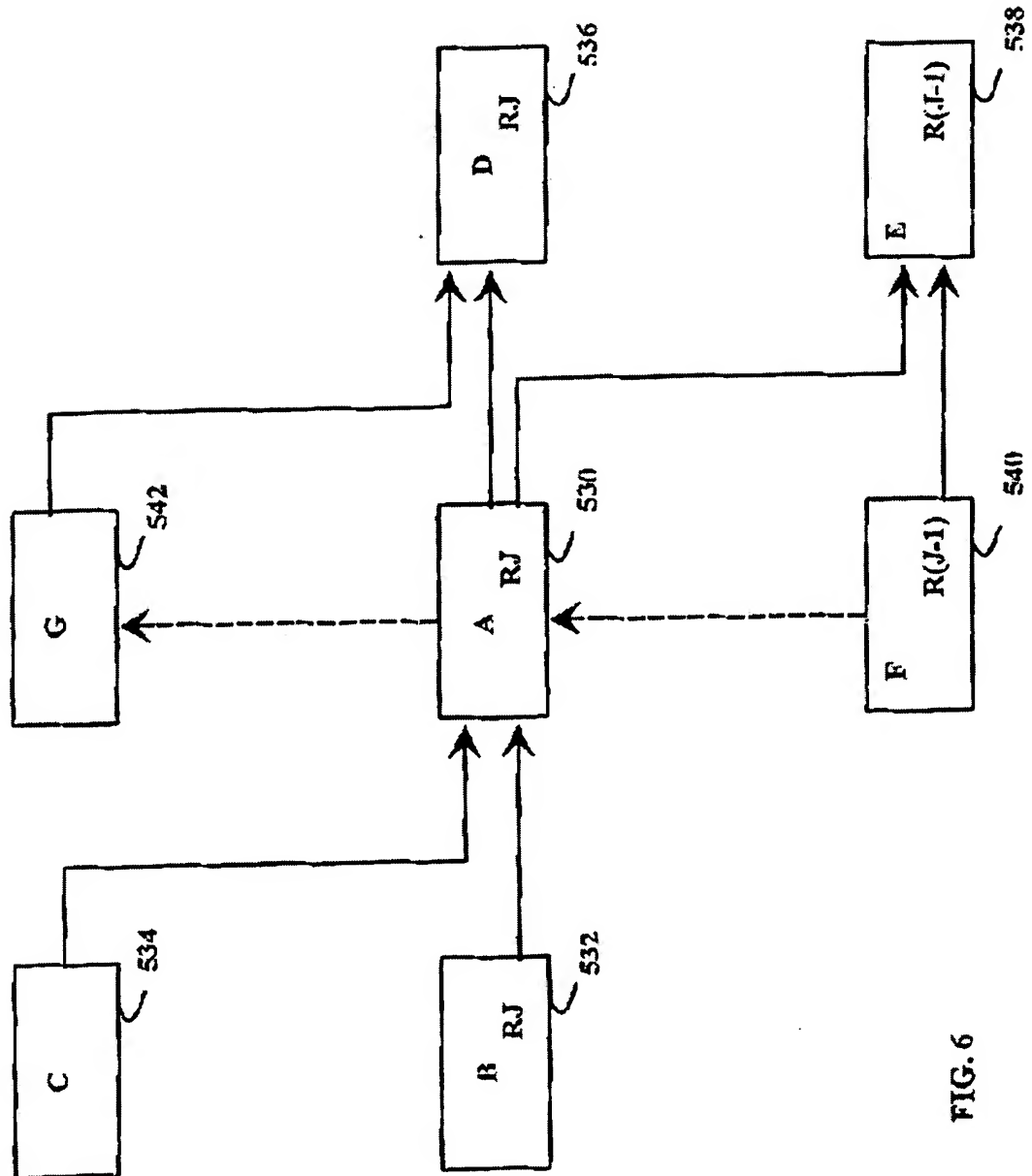


FIG. 4



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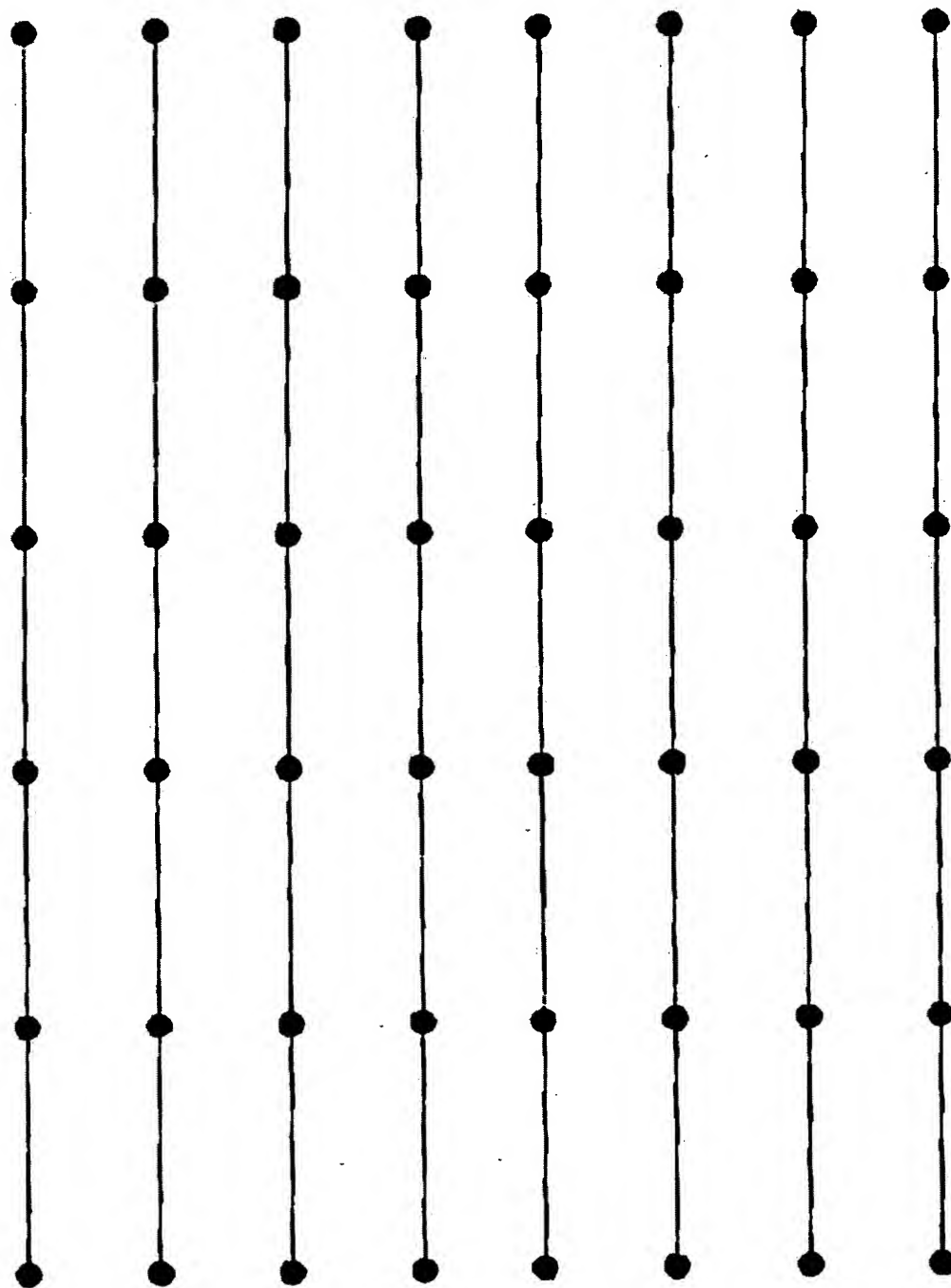


FIG. 7

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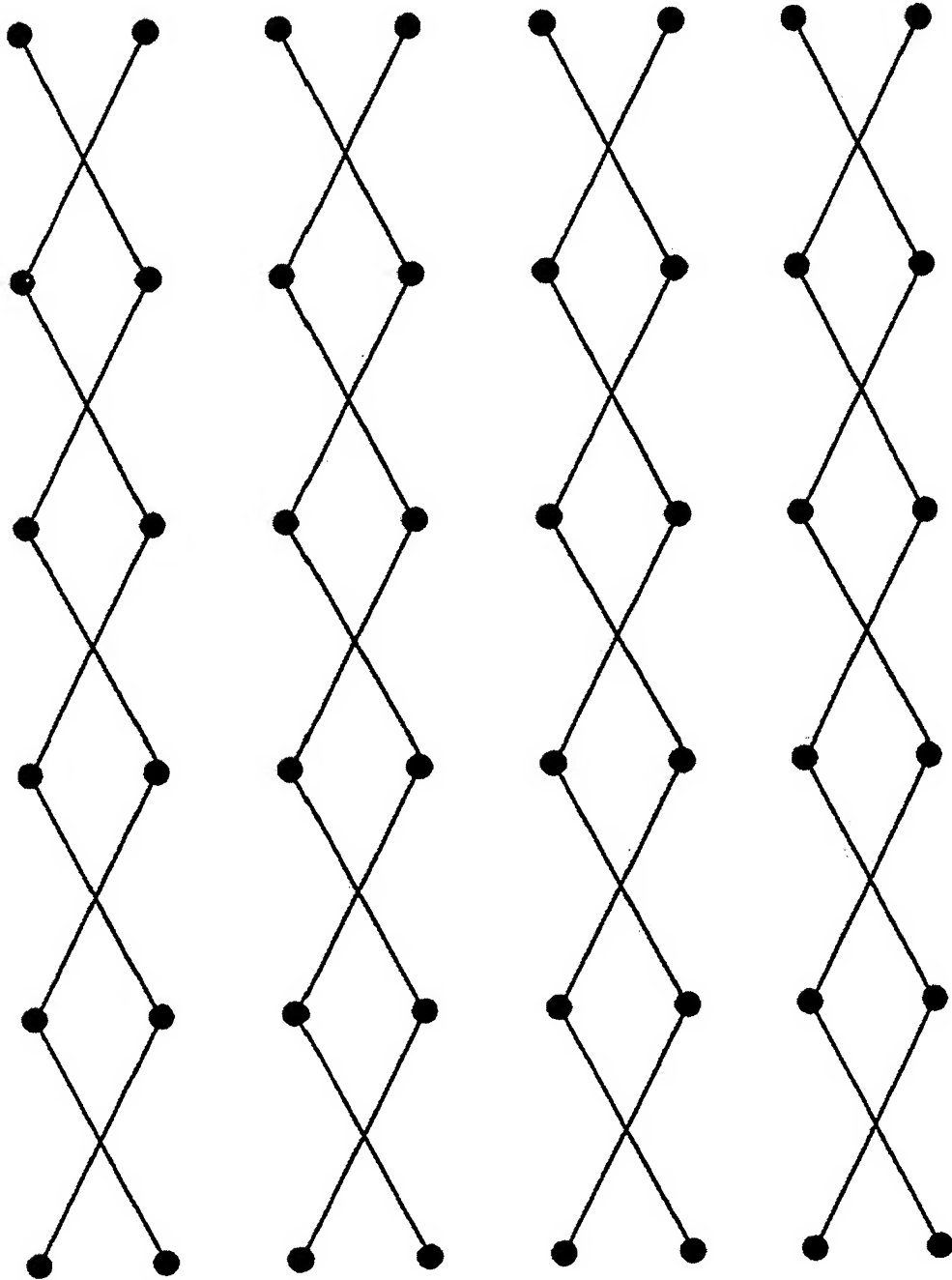


FIG. 8

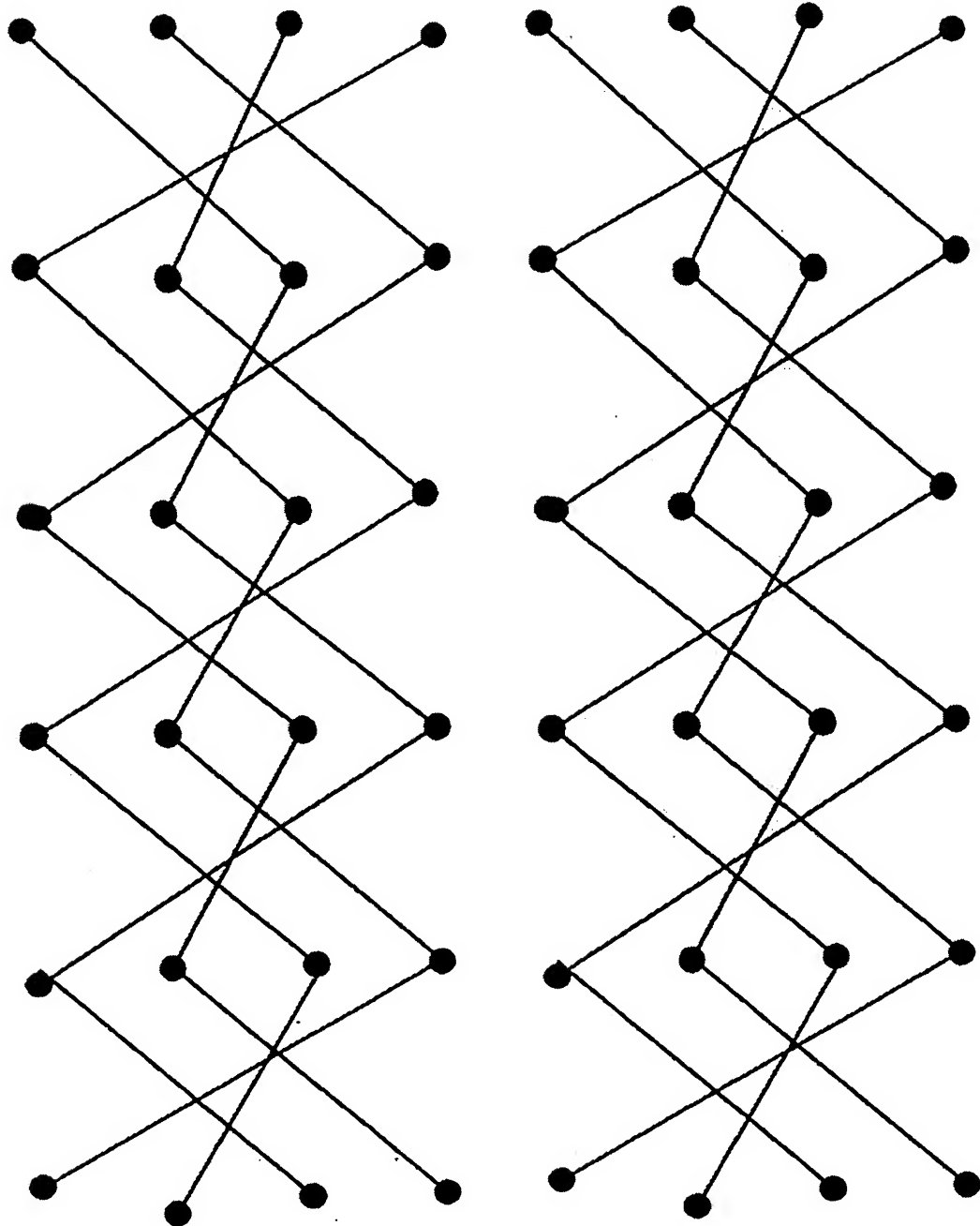


FIG. 9

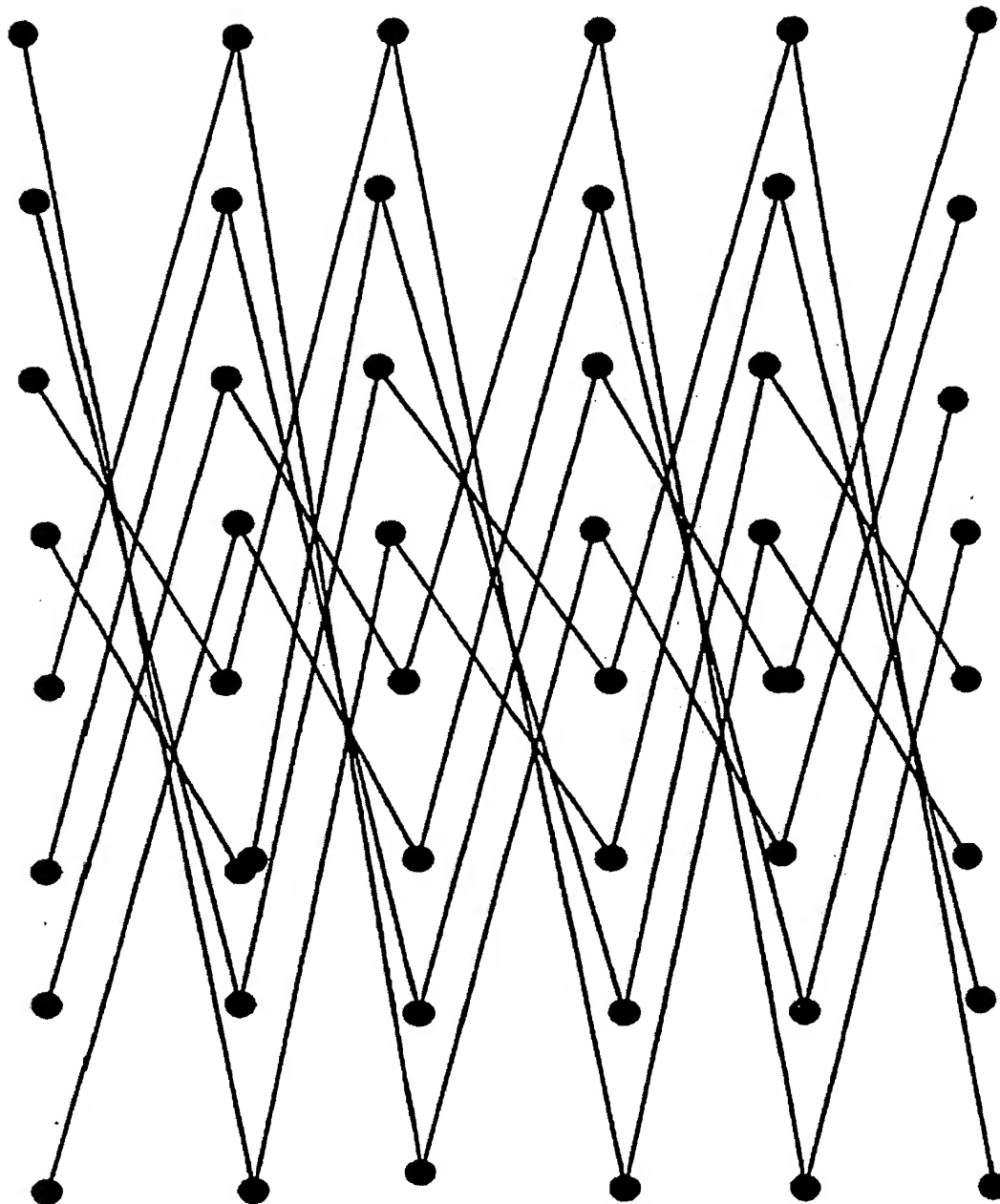
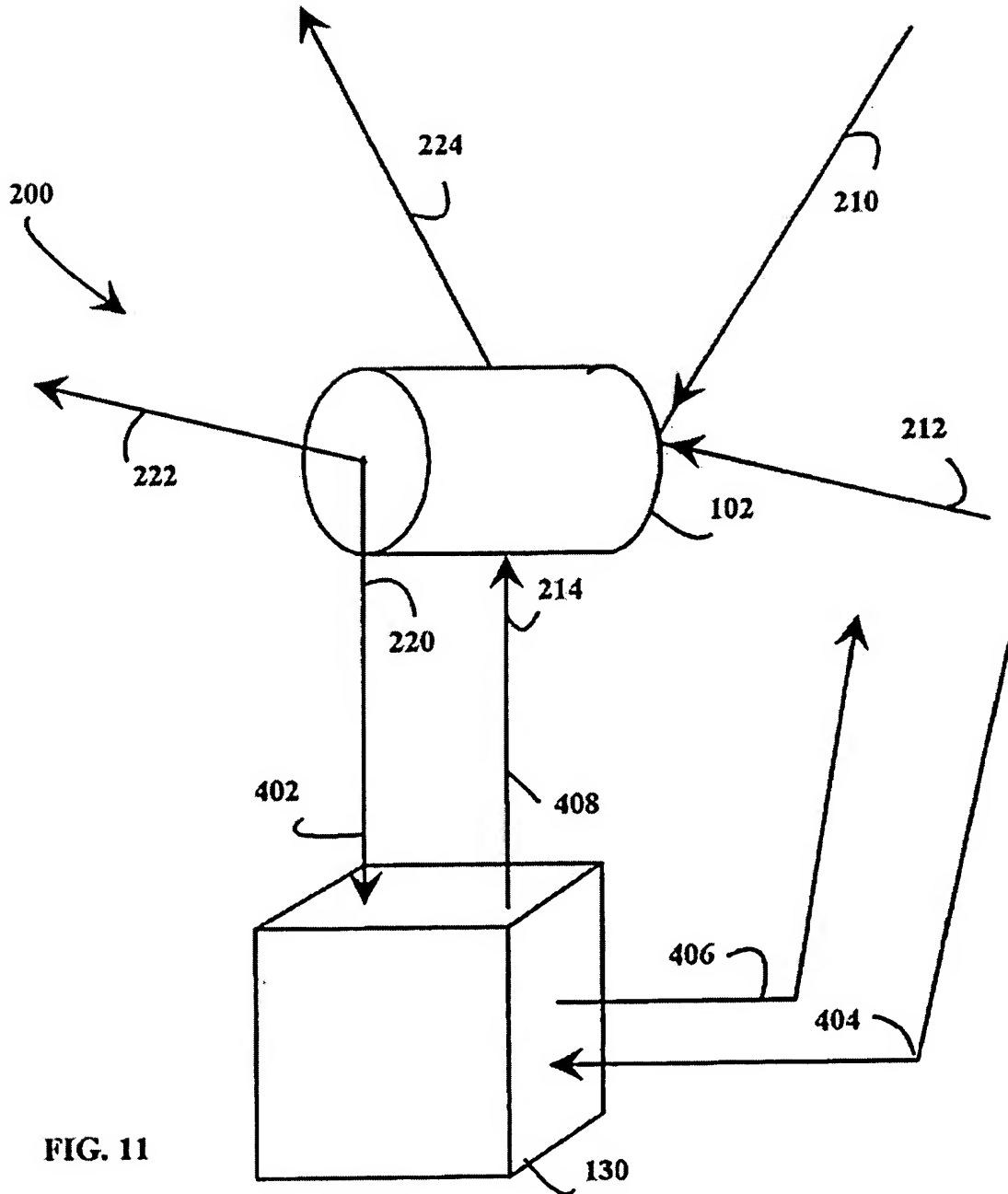
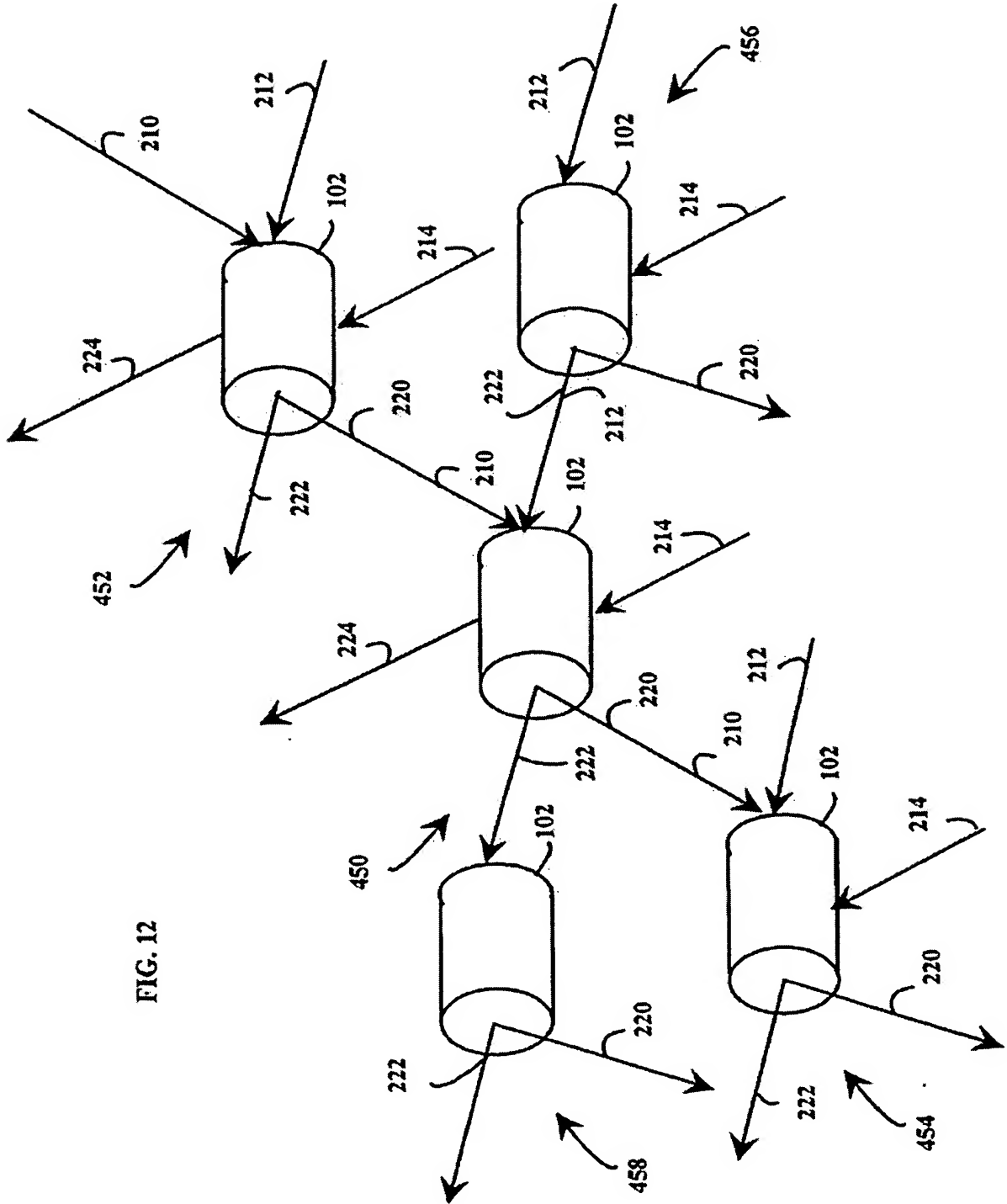


FIG. 10



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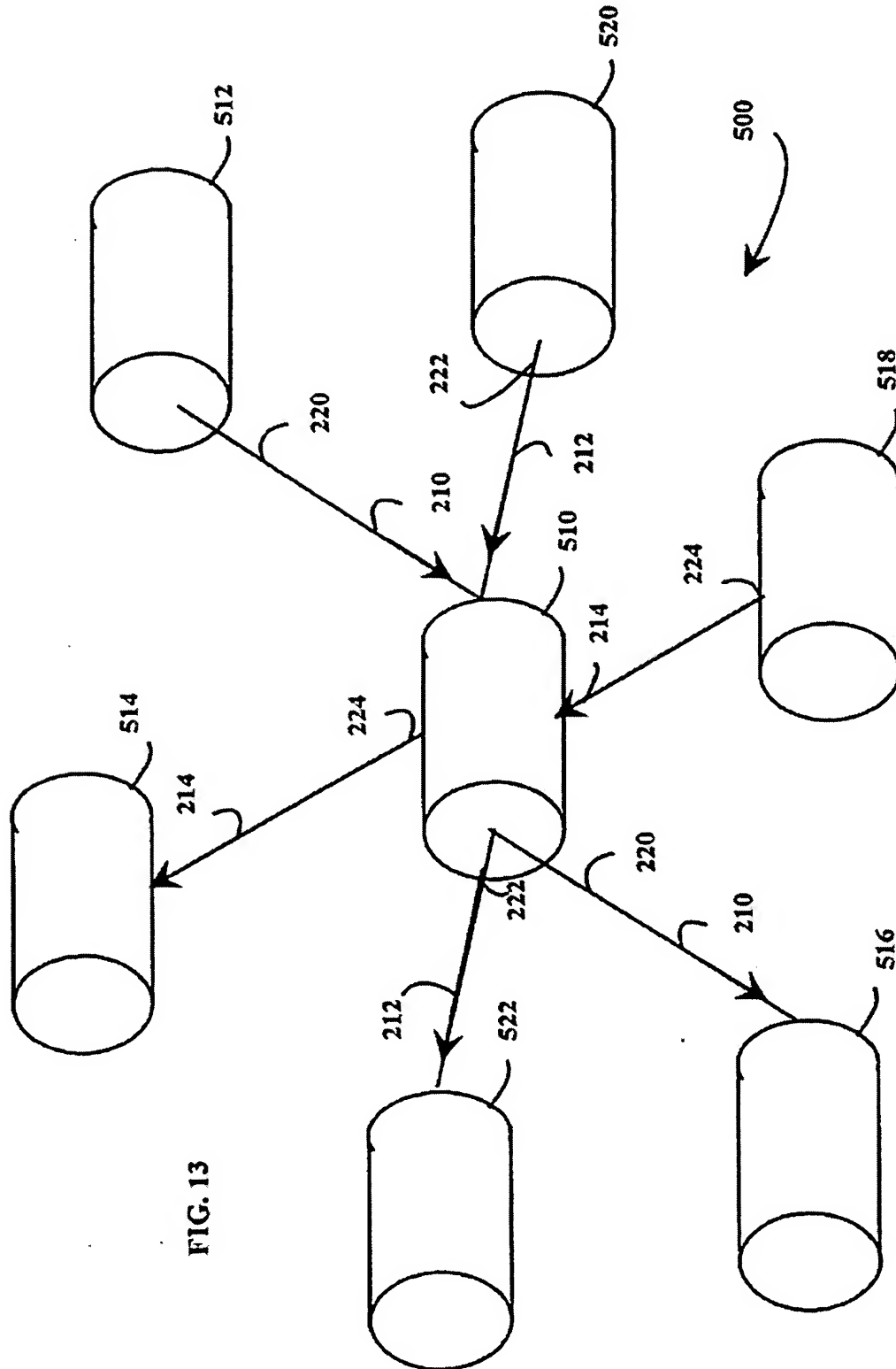


FIG. 13

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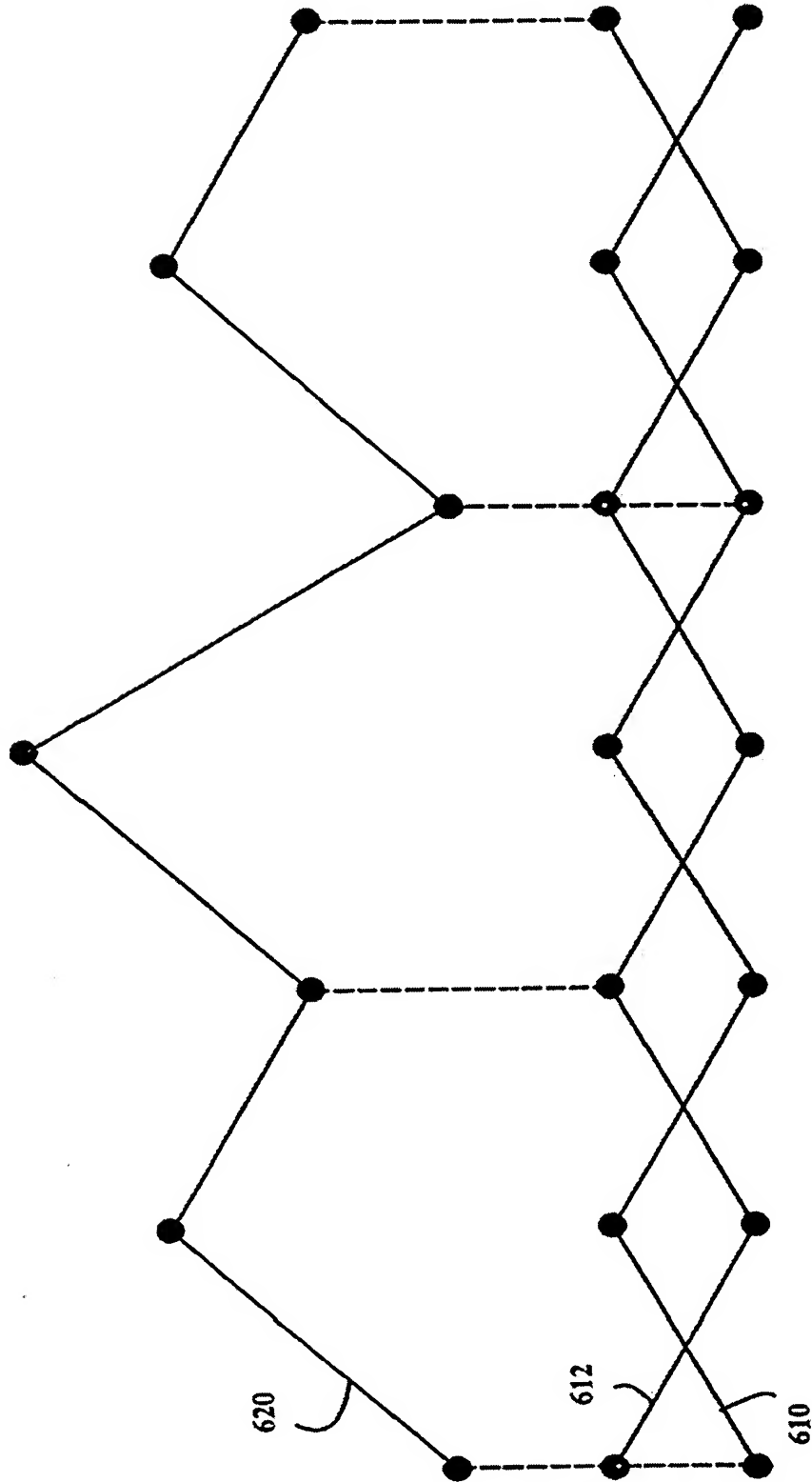
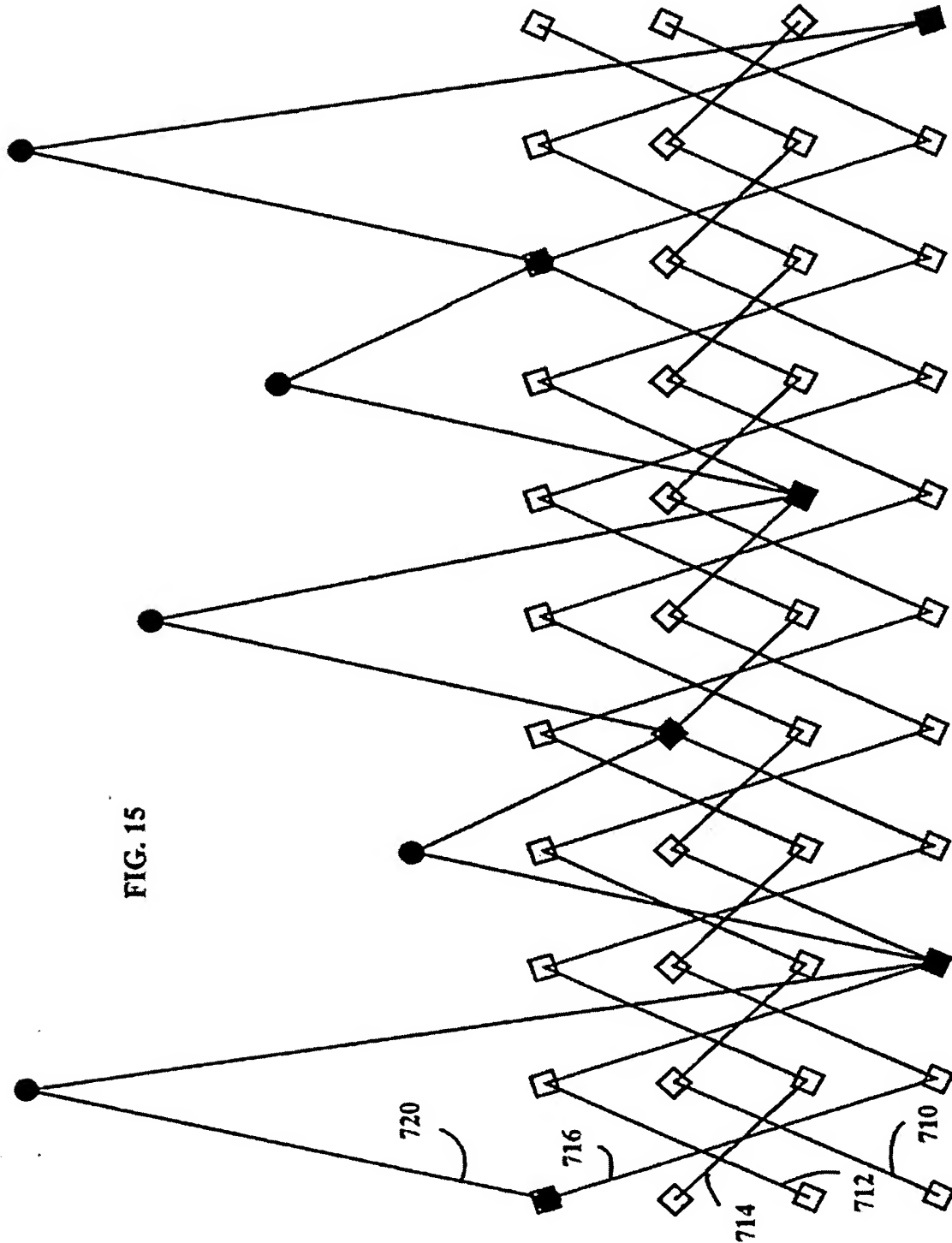
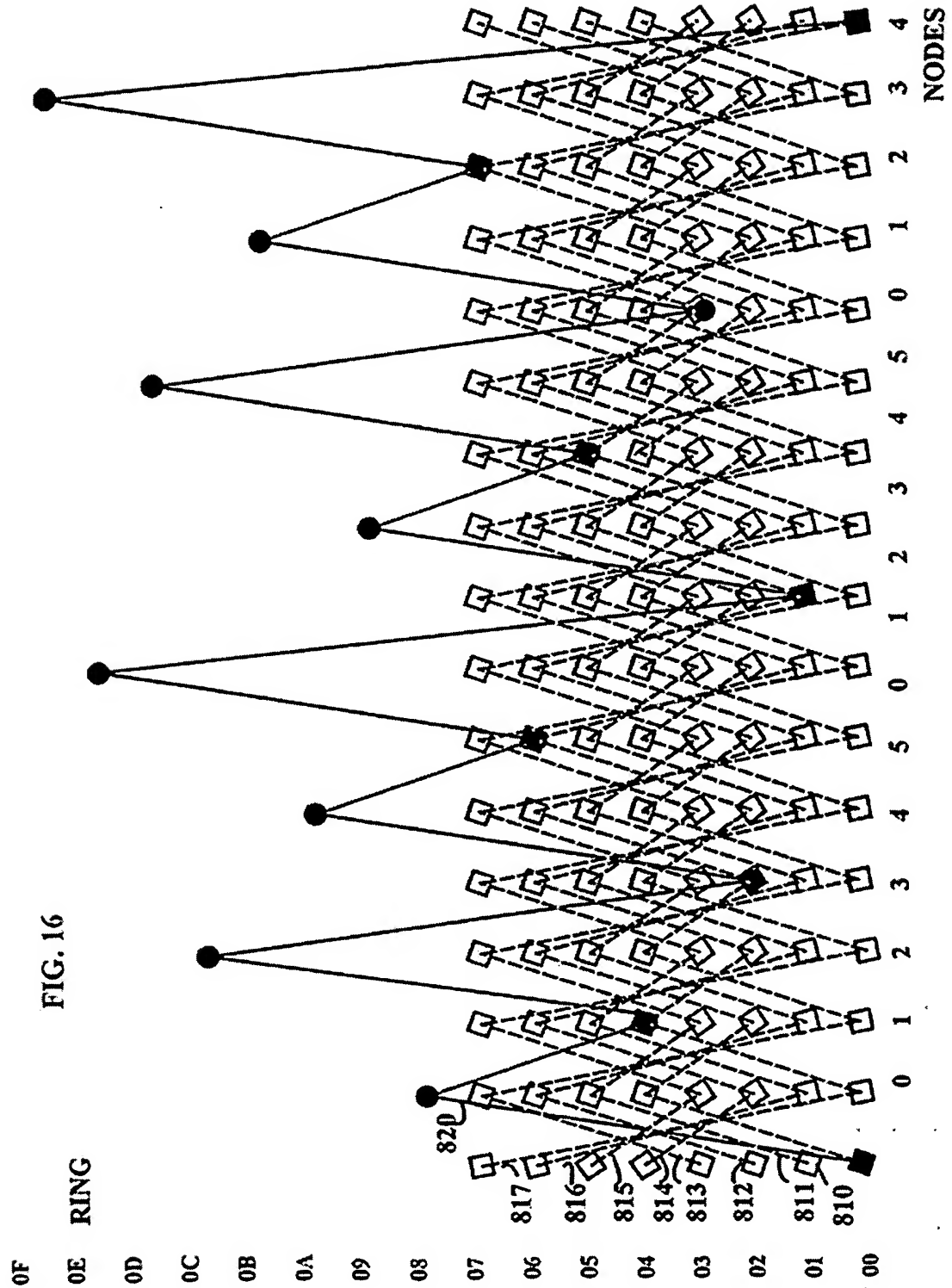


FIG. 14

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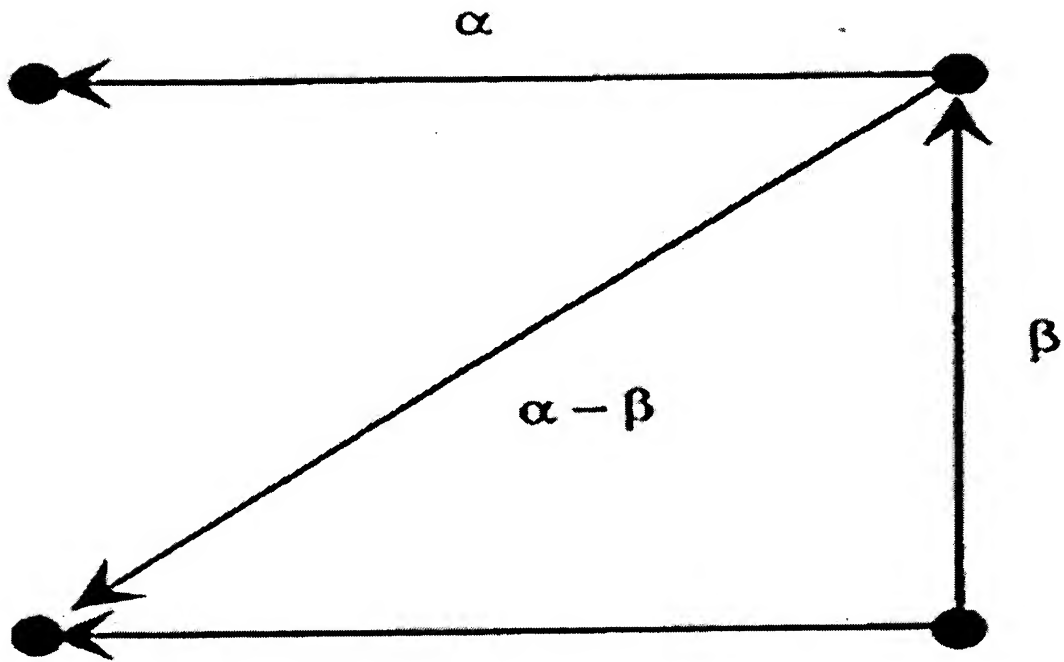
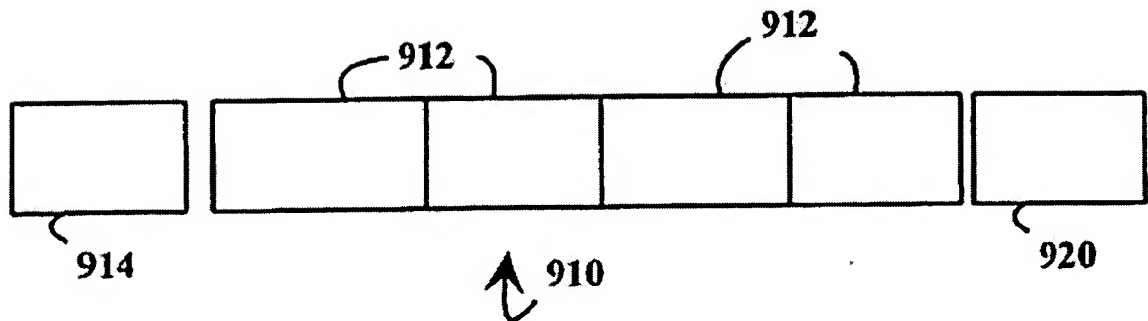


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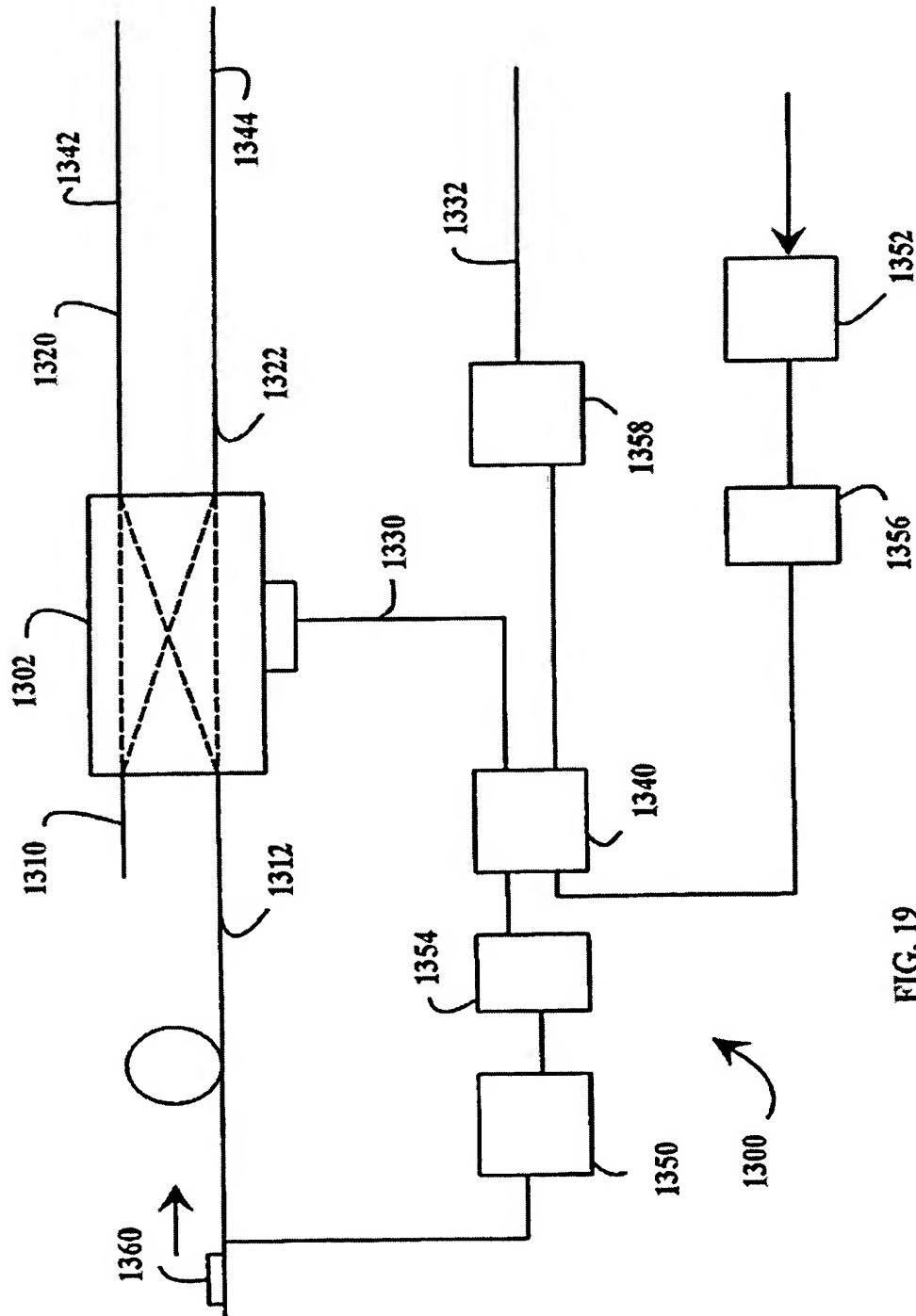


FIG. 19

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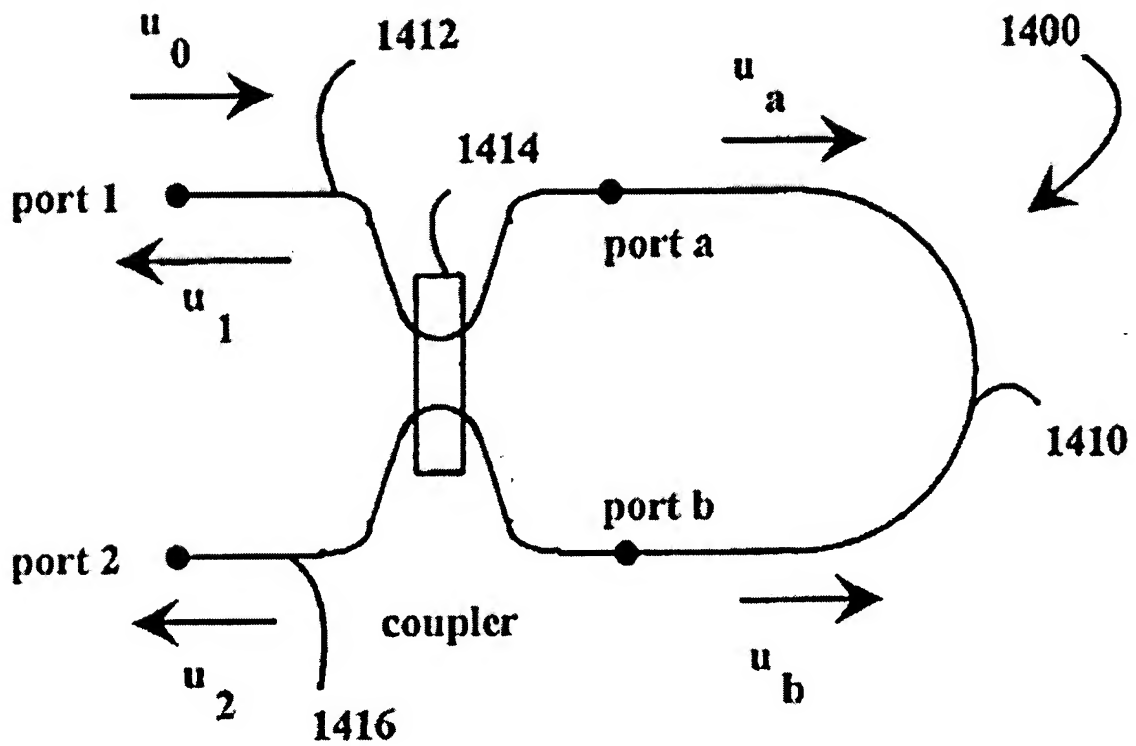


FIG. 20

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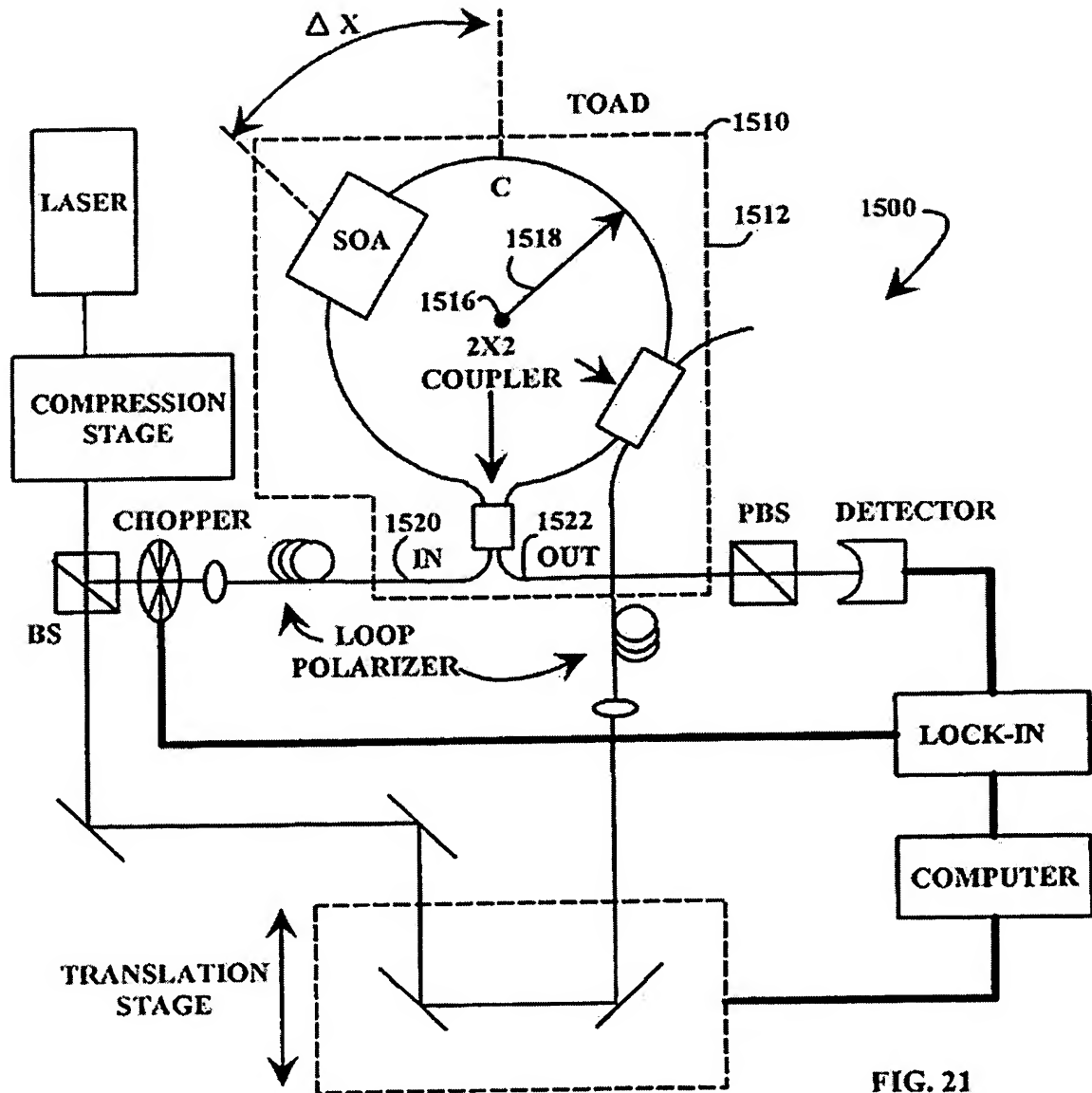
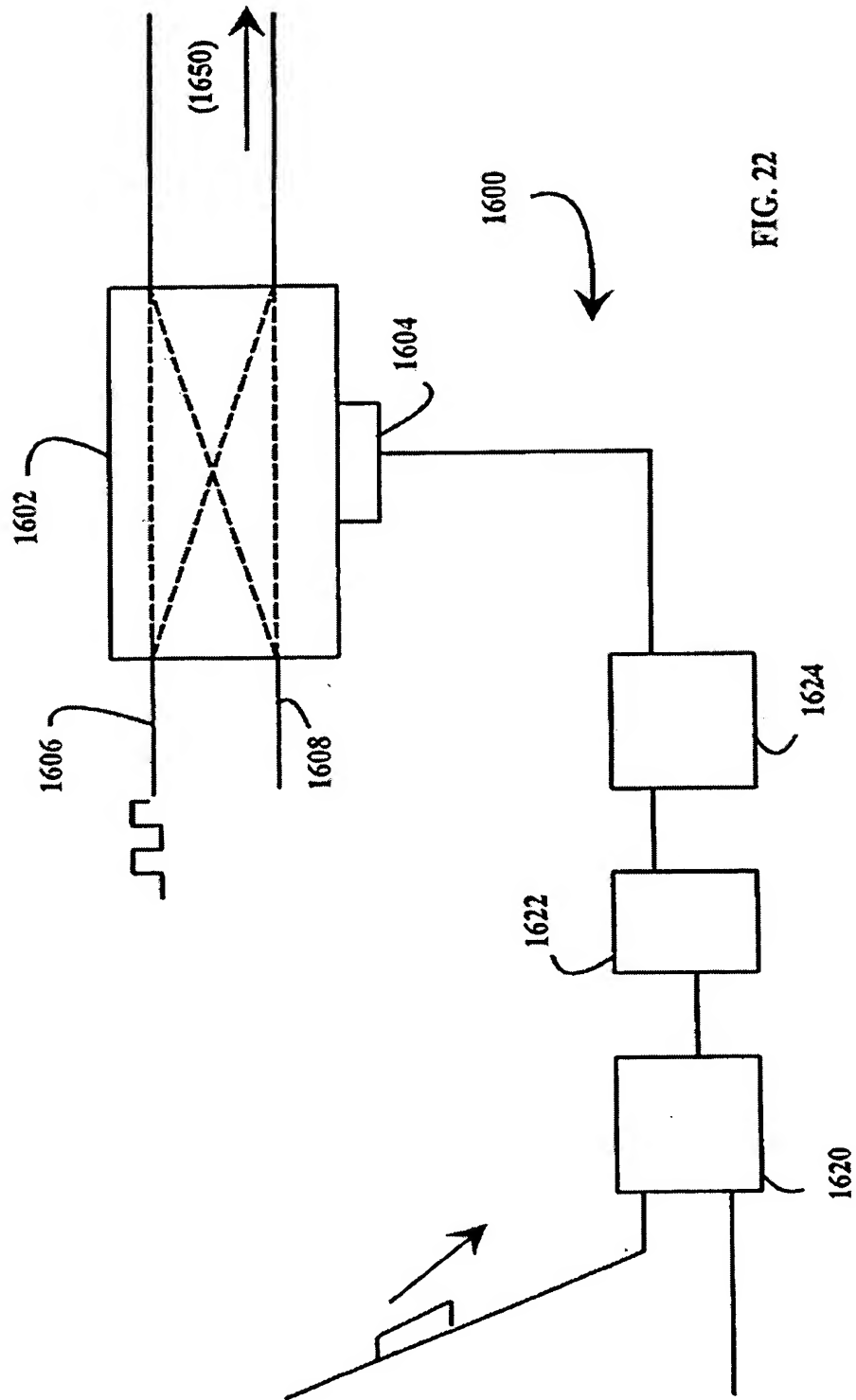


FIG. 21

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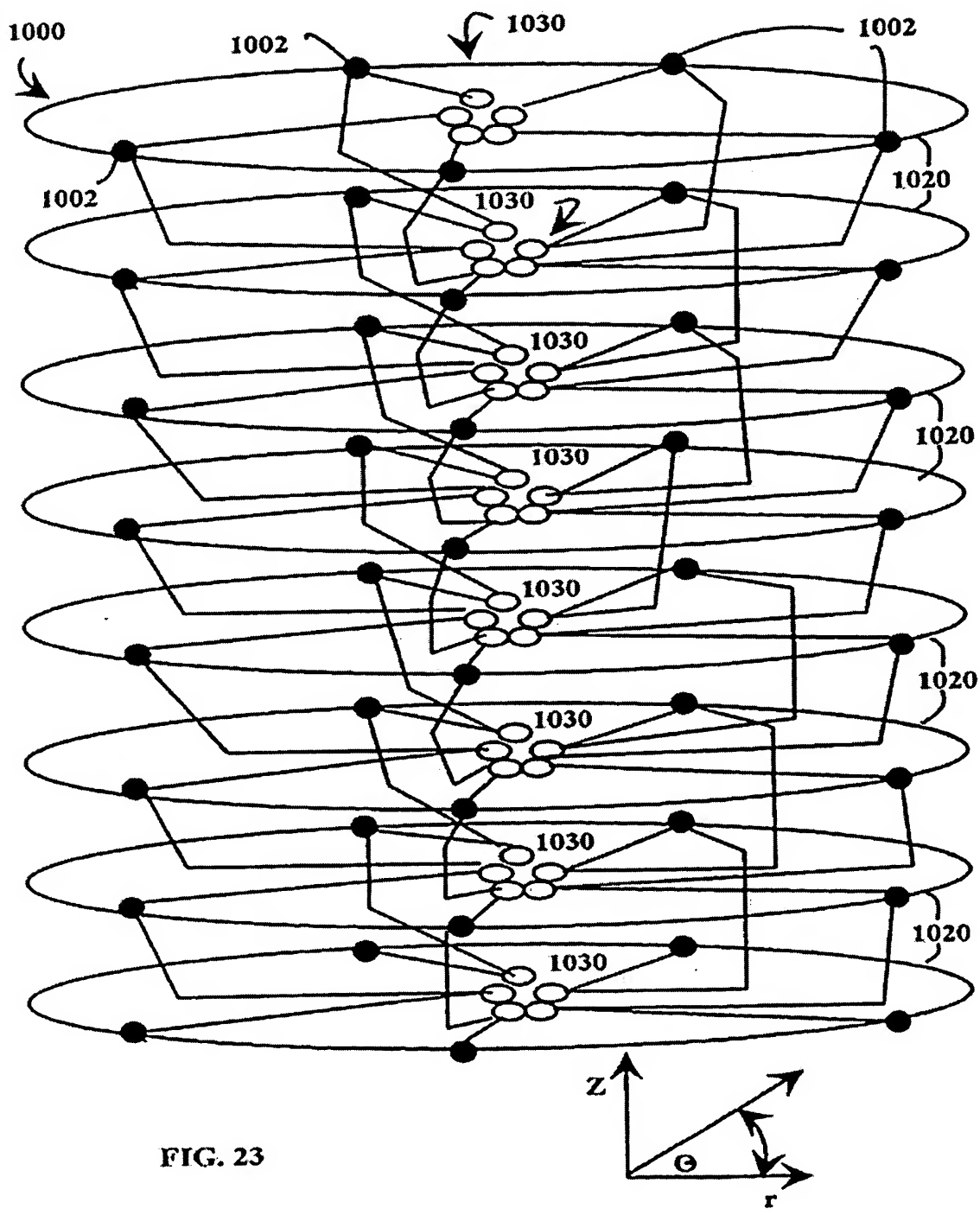
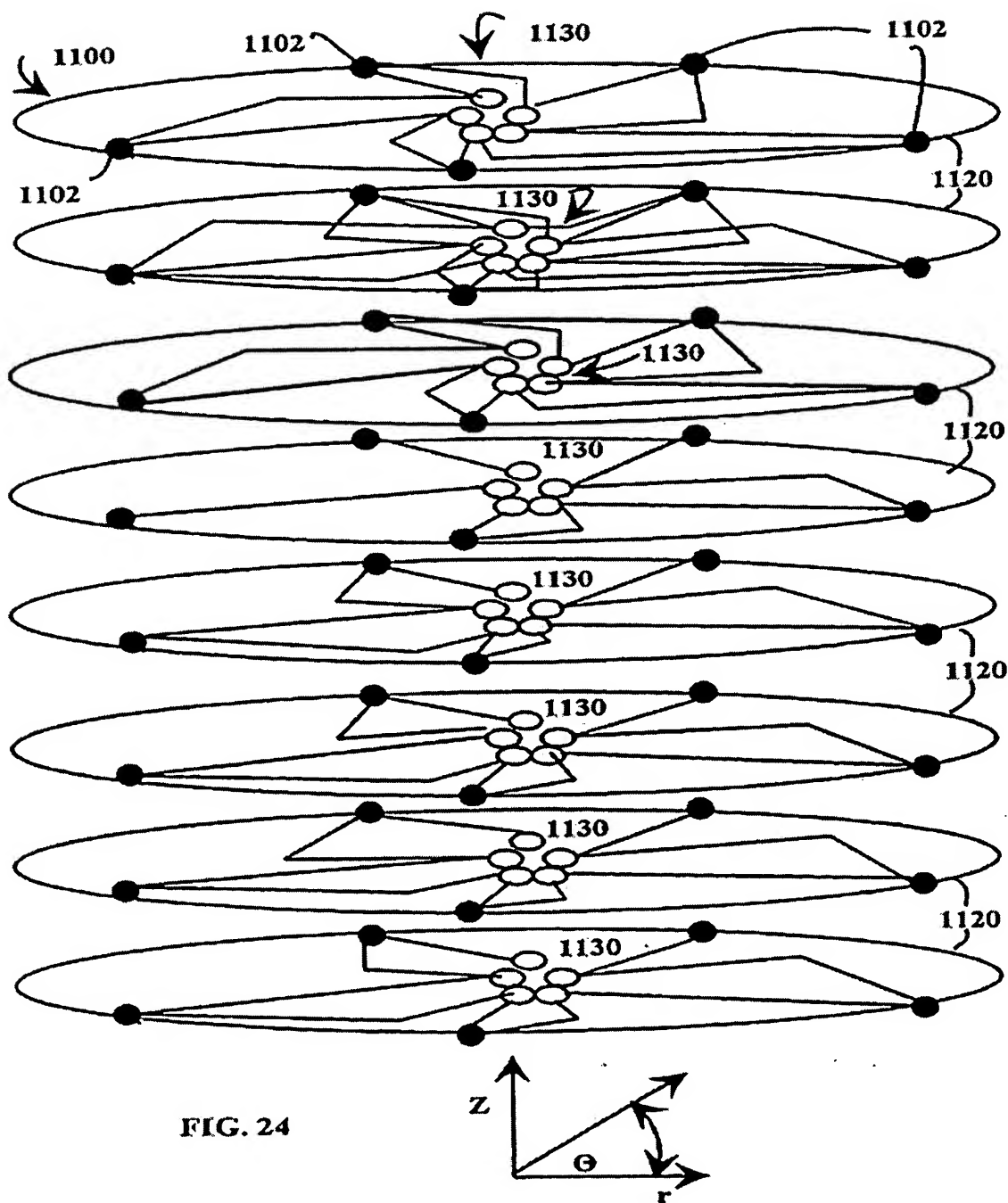


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